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DATE:	January 3, 2006	1 .1
FROM:	Morgan S. Heller II	1 ./11
OPERATOR;	Karen Jeffer	1 N/54
CLIENT/MATTER:	02016-00154	1 11 1
PAGES:	13 (including cover)	

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Comments

Response to Office Action – Serial No.: 10/604,995

BTV.255986.1

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Bednar et al.

Serial No.: 10/604,995

Filed: August 29, 2003

Title: Integrated Circuit Chip Having A Ringed

Wiring Layer Interposed Between A Contact

Layer And A Wiring Grid

Group Art Unit: 2825

Examiner: Yelena Rossoshek

Attorney Docket No.: BUR920020107US1

(02016-00154)

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Morgan S. Heller II

January 3, 2006

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Response To Office Action Transmittal

Transmitted herewith for filing is a Response to First Non-Final Office Action regarding the above-identified Application.

No additional fee is required.

The fee has been calculated as shown below:

P248-10/03

Page 1 of 2

Law Offices of Downs Rachlin Martin PLLC 199 Main Street P.O. Box 190 Burlington, VT 05402-0190 (802) 863-2375

CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
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INDEPENDENT 3	MINUS	3	= 0	x \$200.00/ \$100.00 =	\$.00
1ST PRESENTATION OF MULTIPLE DEP. CLAIMS			\$360.00/ \$180.00 =	\$.00	
				TOTAL	\$.00

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- Any additional filing fees required under 37 C.F.R. §1.16.
- Any patent application processing fees under 37 C.F.R. §1.17.

Respectfully submitted,

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BTV.464074.1

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RESPONSE TO FIRST NON-FINAL OFFICE ACTION

This is in response to the Office Action (paper no. 09262005) mailed from the United States Patent and Trademark Office on October 3, 2005, with respect to the above-identified application. A Status of the Claims, an Amendment to the Specification and Remarks concerning the Office Action start, respectively, on pages 2, 6 and 7.